



Vidyavardhaka Sangha®, Mysore  
**VIDYAVARDHAKA COLLEGE OF ENGINEERING**

Autonomous Institute, Affiliated to Visvesvaraya Technological University, Belagavi  
 (Approved by AICTE, New Delhi & Government of Karnataka)  
 Accredited by NBA | NAAC with 'A' Grade

Department of Electrical & Electronics Engineering

Phone: +91 821-4276240, Email: hodee@vvce.ac.in

Web: <http://www.vvce.ac.in>



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**TIME TABLE FOR THE ACADEMIC YEAR 2020 - 2021 (ODD SEMESTER)**

SEM: III(A)

Version 1.0

Room No: C 105

Day/Time	9.00 – 9.40	9.45 – 10.25	10.25 - 11.00	11.00 – 11.40	11.45-12.25	12.30-1.10	2 - 3
MON	18EE32	18EE34	BREAK	18EE36	18EE33	18MAT31	
TUE	18EE33	18EE35		18EE32	18MAT31	18CPH39	18EEL37 (B1) (Dr. KGR + UM) /18EEL38 (B2) (PHK + RP)
WED	18EE34	18EE33		18EE36	18EE32	LUNCH BREAK	18EEL37 (B2) (Dr. KGR + UM) /18EEL38 (B3) (PHK + RP)
THUR	18MAT31	18EE32		18EE33	18EE35		18EEL37 (B3) (Dr. KGR + UM) /18EEL38 (B4) (PHK + RP)
FRI	18EE35	18MAT31		18EE34	18EE36		18EEL37(B4) (Dr. KGR + UM) / 18EEL38 (B1) (PHK+SGK)
SAT	18MAT31	18EE35		18EE36	18EE34		

Time Table Coordinators: Prof. AVINASH R & Prof. ANOOP H A

18MAT31-Transform Calculus, Fourier series & Numerical Techniques	Prof. MR	18EE36-Electrical and Electronic Measurements	Prof. MJ
18EE32 - Electric Circuit Analysis	Prof. GB	18EEL37 - Electrical Machines Laboratory-I	Dr. KGR & Prof. UM
18EE33 - Transformers and Generators	Prof. UM	18EEL38 - Electronics Laboratory	Prof. RP & Prof. PHK
18EE34 - Analog Electronic Circuits	Prof. PHK	18CPH39-Constitution of India, Professional Ethics & Cyber law	Prof. SS
18EE35 - Digital System Design	prof. RP		

*Shobha Shankar*  
 (Dr. SHOBHA SHANKAR)  
 Head of the Department  
 Professor & Head  
 Dept. of Electrical & Electronics Engg.  
 Vidyavardhaka College of Engineering  
 Mysore - 570 002

*Dayananda H S*  
 (Dr. H S DAYANANDA)  
 Dean (Academics)  
**Dr. DAYANANDA H S**  
 Professor of Civil Engineering &  
 Dean - Academics  
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*B S Sadashive Gowda*  
 (Dr. B SADASHIVE GOWDA)  
 PRINCIPAL  
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**TIME TABLE FOR THE ACADEMIC YEAR 2020 - 2021 (ODD SEMESTER)**

SEM: V

Version 1.0

Room No: C 104

Day/Time	9.00 – 9.40	9.45 – 10.25	10.25 - 11.00	11.00 – 11.40	11.45-12.25	12.30-1:10	2:00 – 3:00
MON	18EE51	18EE52	BREAK	18EE53	18EE54	LUNCH BREAK	18EEL57 (B1) (Dr. LC + CG) /18EEL58 (B2) (Dr. GN+ AR)
TUE	18EE53	18EE51		18EE52	18EE55		18EEL57 (B2) (Dr. RS + Dr. LC) /18EEL58 (B3) (Dr. GN + AR)
WED	18EE52	18EE54		18EE56	18EE53		18EEL57 (B3) (Dr. RS + Dr. LC) /18EEL58 (B4) (Dr. GN + AR)
THUR	18EE54	18EE53		18EE55	18EE51		18EEL57 (B4) (Dr. RS + CG) /18EEL58 (B1) (Dr. GN + AR)
FRI	18EE56	18EE55		18EE54	18EE56		
SAT	18EE55	18EE56		18EE51	18EE52	18CIV59	

Time Table Coordinators:

Prof. AVINASH R & Prof. ANOOP H A

18EE51 - Management and Entrepreneurship	Prof. AR	18EE56 - High Voltage Engineering (BRP)	Prof. BRP
18EE52 - Microcontroller	Dr. RS	18EEL57- Microcontroller Laboratory (Dr. LC+CG)	Dr. RS & Dr. LC & Prof. CG
18EE53 - Power Electronics	Dr. GN	18EEL58 - Power Electronics Laboratory	Dr. GN & Prof. AR
18EE54 - Signals and Systems	Prof. AHA	18CIV59 – Environmental Studies	Prof. GP
18EE55 - Electrical Machine Design	Prof. MJR		

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Dr. DAYANANDA H S  
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**TIME TABLE FOR THE ACADEMIC YEAR 2020 - 2021 (ODD SEMESTER)**

SEM: VII

Version 1.0

Room No: C 103

Day/Time	9.00 – 9.40	9.45 – 10.25	10.25 - 11.00	11.00 – 11.40	11.45-12.25	12.30-2:00	2:00 – 3:00
MON	17EE742	17EE73	BREAK	17EE71	17EE72	LUNCH BREAK	17EEL77 (B1) (BRP + MJR)/ 17EEL76 (B2) (MJ + GB)
TUE	17EE71	17EE752		17EE73	17EE742		17EEL77 (B2) (BRP + MJR)/ 17EEL76 (B3) (MJ + GB)
WED	17EE73	17EE71		17EE72	17EE752		17EEL77 (B3) (BRP + MJR)/ 17EEL76 (B4) (MJ + GB)
THUR	17EE752	17EE742		17EE72	17EE73		17EEL77 (B4) (BRP + MJR)/ 17EEL76 (B1) (Dr. SS + GB)
FRI	17EE72	17EE752		17EE742	17EE71		
SAT							

Time Table Coordinators: Prof. AVINASH R & Prof. ANOOP H A

17EE71 - Power System Analysis-2	Dr. SS	17EEL76-Power System Simulation Laboratory	Prof. MJ & Prof. GB
17EE72 - Power System protection	Prof. CG	17EEL77-Relay and High Voltage Laboratory	Prof. BRP & Prof. MJR
17EE73- High Voltage Engineering	Prof. BRP	17EEP78-Project Phase-1+Seminar	
17EE742 - Utilization of Electrical Power	Prof. SGK		
17EE752-Testing and Commissioning of Power System Apparatus	Dr. KGR		

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